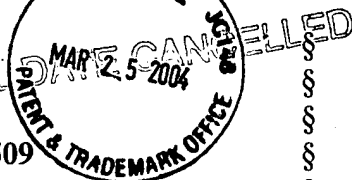




IN THE UNITED STATES PATENT & TRADEMARK OFFICE

In re Application of:
DON MCCORD



Attorney Docket No.: 37802PCT-US

Serial No.: 09/980,509

Examiner: ASSOUD, PATRICK J.

Filed: OCTOBER 19, 2001

Title: **METHOD AND SYSTEM FOR
WAFER AND DEVICE-LEVEL
TESTING OF AN INTEGRATED
CIRCUIT**

Group Art Unit: 2857

INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 C.F.R. § 1.197(b)

Commissioner for Patents
PO Box 1450
Alexandria, Virginia 22313-1450

Sir:

Applicants submit the following information on (Substitute) Form PTO-1449 listing references pursuant to 37 CFR 1.97(b). Copies of the references listed on (Substitute) Form PTO-1449 are included herewith. .

A check for \$180.00 is included herewith.

03/29/2004 AADDF01 00000001 09980509
02 FC:1806 180.00 OP

Respectfully submitted,

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03/29/2004 AADDF01 00000002 09980509
02 FC:1806 180.00 OP

CERTIFICATE OF MAILING
37 C.F.R. § 1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service on the below listed date with sufficient postage for first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date: 3/23/04

By:
Signature

[illegible]

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant